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PATENTS

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicant(s):** Katsutoshi Seki                      **Examiner:** Unassigned  
**Serial No:** To be assigned                      **Art Unit:** Unassigned  
**Filed:** Herewith                      **Docket:** 14954  
**For:** SELF ORTHOGONAL                      **Dated:** September 26, 2001  
DECODING CIRCUIT AND  
SELF ORTHOGONAL DECODING METHOD

#3

Assistant Commissioner for Patents  
United States Patent and Trademark Office  
Washington, D.C. 20231

**INFORMATION DISCLOSURE STATEMENT**

Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98, it is requested that the following reference, which is also listed on the attached Form PTO-1449, be made of record in the above-identified case.

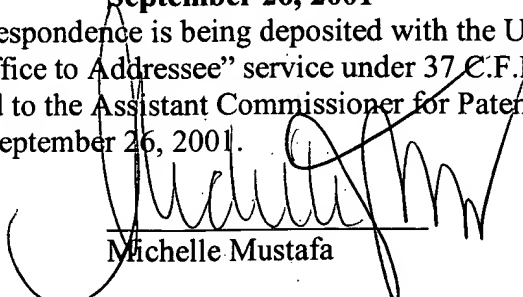
1. Hideki Imai, "Coding Theory", The Institute of Electronics, Information and Communication Engineers, pp. 274-278 (1990).

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I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. § 1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents and Trademarks, Washington, D.C. 20231 on September 26, 2001.

Dated: September 26, 2001

  
Michelle Mustafa

Applicant is submitting a copy of the above-cited reference. The relevance of the above-identified reference has been described in the specification.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no statement or fee is required.

Respectfully submitted,



Paul J. Esatto, Jr.

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